



75TH
ANNIVERSARY



IEEE
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SHAHEED RAJGURU COLLEGE OF APPLIED SCIENCES FOR WOMEN
UNIVERSITY OF DELHI
NAAC GRADE A+ | NIRF 2023 RANK -32 (COLLEGE CATEGORY)
DEPARTMENT OF INSTRUMENTATION & DEPARTMENT OF ELECTRONICS

In collaboration with
IEEE Electron Devices Society, Delhi Chapter
along with
IEEE Delhi Section and IEEE Delhi Section Women in Engineering Affinity Group

PRESENTS

HANDS-ON WORKSHOP ON PCB DESIGNING AND FABRICATION

DATES

4th Oct. To 6th Oct. 2023



TIME

09:00 AM - 05:30 PM



VENUE

Seminar Room, Ground
Floor, SRCASW,
Vasundhara Enclave,
Delhi- 110096



**IMPLICIT LEARNING
EXPERIENCE**

QUERIES MAY BE SENT TO

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WORKSHOP HIGHLIGHTS -

- INTRODUCTION AND USE OF KICAD DESIGNING TOOL FOR PCB.
- PCB FABRICATION.
- TESTING AND IMPLEMENTATION OF CIRCUITS.

Note: Certificates will be provided after the successful completion of the workshop.



Hands-on Workshop on PCB Designing and Fabrication

Technical Program Schedule

Day 1: Wednesday, 4 th October 2023		
Time	Venue	Program Details
Inaugural Session: Chief Guest: Sr. Prof. Mridula Gupta, Former Head, Dept. of Electronic Science, UDSC		
10:00 am-10:15 am	Seminar Room (Ground Floor)	Lamp Lighting
10:15 am-10:30 am		Welcome Address by the Principal Prof. Payal Mago
10:30 am- 11:00 am		Talk on “ <i>Overview of IEEE and IEEE EDS Delhi Chapter</i> ” Sr. Prof. Mridula Gupta, Former Head, Department of Electronic Science, University of Delhi
11:00 am-12:30 pm	Seminar Room (Ground Floor)	“ <i>Tinkering Your Way to Prosperity: Science, Technology, Society and National Interests.</i> ” Mr. Dhananjay V. Gadre, Associate Professor, Netaji Subhash University of Technology, Delhi
12:30 pm-1:15 pm	Lunch Break	
Technical Session-I		
1:15 pm -5:30 pm	Seminar Room (Ground Floor)	“ <i>PCB Designing Using Software Kicad</i> ”, Mr. Arun, Director, SincGrid Pvt. Ltd.
Day 2: Thursday,5 th October 2023		
Technical Session-I		
9:00 am-1:00 pm	Seminar Room (Ground Floor)	“ <i>PCB Designing Using Software Kicad</i> ”, Mr. Arun, Director, SincGrid Pvt. Ltd.
1:00 pm-1:45 pm	Lunch Break	
Technical Session-II		
1:45 pm-5:30 pm	Communication Lab (Electronics Department)	“ <i>PCB Fabrication Using Toner Transfer Method</i> ”, Mr. Arun and Team, Director, SincGrid Pvt. Ltd.
Day 3: Friday,6 th October 2023		
Technical Session-I		
9:00 am-1:00 pm	Communication Lab (Electronics Department)	“ <i>Hands on Training on circuit designing</i> ”, Mr. Arun and Team, Director, SincGrid Pvt. Ltd.
1:00 pm-1:45 pm	Lunch Break	
Technical Session-II		
1:45 pm -3:30 pm	Communication Lab (Electronics Department)	“ <i>Testing of Circuits</i> ”, Mr. Arun and Team, Director, SincGrid Pvt. Ltd.
Valedictory Session		
3:30 pm-4:00 pm	Seminar Room (Ground Floor)	Talk on “ <i>75 years of Transistors</i> ” , by Guest of Honour, Prof. Manoj Saxena, Department of Electronics, Deen Dayal Upadhyaya College, University of Delhi & Chairperson-IEEE EDS Delhi Chapter
4:00 pm-4:30 pm		Address by Guest of Honour, Prof. A.Q Ansari, Dept. of Electrical Engineering, JMI & Chair Designate-IEEE Delhi Section
4:30 pm-5:15 pm		Certificate Distribution
5:15 pm-5:30 pm		Vote of Thanks