Course: B.Sc. (Hons) Electronics I Semester wef 20-07-2019

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| **Time** | **9:00-10:00** | **10:00-11:00** | **11:00-12:00** | **12:00-1:00** | **1:00-1:30** | **1:30-2:30** | **2:30-3:30** | **3:30-4:30** | **4:30-5:30** |
| **Day** | **1** | **2** | **3** | **4** | **L**  **U**  **N**  **C**  **H**  **B**  **R**  **E**  **A**  **K** | **5** | **6** | **7** | **8** |
| **Monday** |  | AECC  RRA  LT -21 | Maths F Elect  JY  LT -21 | GE | Maths F Elect Practical (Group – B)  JY  E\_Sim 2 Lab | |  | |
| Basic Circuit Theory and Network Analysis Practical (Group – C)  SA  Circuits & Network Lab | | | |
| **Tuesday** | Maths F Elect Practical (Group – B) JY  E\_Sim 2 Lab | | Ckt Th & NA  SA  LT -21 | GE | GE Practical | | | |
| **Wednesday** |  |  |  | Maths F Elect  JY  LT -21 | AECC  RRA  LT -21 | Ckt Th & NA  SA  LT -21 |  |  |
| **Thursday** | AECC  RRA  LT -21 | GE | Ckt Th & NA  SA  LT -21 | Maths F Elect  JY  LT -21 | Basic Circuit Theory and Network Analysis Practical (Group – B)  DJA  Circuits & Network Lab | | | |
| Maths F Elect Practical (Group – C) JY  E\_Sim1 Lab | |  | |
| **Friday** | GE |  | Maths F Elect  JY  LT -21 | Ckt Th & NA  SA  LT -21 | AECC  RRA  LT -21 | Maths F Elect Practical (Group – C)  JY  E\_Sim1 Lab | |  |

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| Subject | Maths F Elect – Mathematics Foundation for Electronics | Ckt Th & NA – Basic Circuit Theory and Network Analysis | | AECC – English | GE – Generic |
| Teacher Code | JY – Ms. Jyoti | SA – Ms. Sonia Ahlawat | DJA – Mr. Deepak Jaiswal | RRA – Mr. Rituraj Anand |  |

Course: B.Sc. (Hons) Electronics III Semester wef 20-07-2019

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| **Time** | **9:00-10:00** | **10:00-11:00** | **11:00-12:00** | **12:00-1:00** | **1:00-1:30** | **1:30-2:30** | **2:30-3:30** | **3:30-4:30** | **4:30-5:30** |
| **Day** | **1** | **2** | **3** | **4** | **L**  **U**  **N**  **C**  **H**  **B**  **R**  **E**  **A**  **K** | **5** | **6** | **7** | **8** |
| **Monday** | GET | GE | C Prog & DS Practical  MT & BS  E\_Sim1 & E\_Sim 2Lab | | C Prog & DS  MT  LT-21 | Dig E & Verilog  NK  LT-21 | Dig E & Verilog Practical  NK & SUA  Digital Lab | |
| **Tuesday** | GE | Dig E & Verilog NK  LT-21 | C Prog & DS  MT  Digital Lab | Electronic Circuits  SA  LT-21 | M App Practical (Group – 1)  BS  E\_Sim 2 Lab | | Dig E & Verilog Practical  (Group – 1)  NK  Digital Lab | |
| Electronic Circuits Practical (Group – 2)  DJA  Analog Lab | | | |
| **Wednesday** | GE | Dig E & Verilog  NK  Digital Lab | C Prog & DS Practical  MT & BS  E\_Sim1 & E\_Sim 2Lab | | Electronic Circuits  SA  Digital Lab | M App  BS  Digital Lab | M App Practical  MT & BS  E Sim 1 & E\_Sim 2 Lab | |
| **Thursday** |  | C Prog & DS  MT  Digital Lab | Dig E & Verilog NK  Digital Lab | Electronic Circuits  SA  Digital Lab | Electronic Circuits Practical (Group – 1)  SA  Analog Lab | | | |
| Dig E & Verilog Practical  (Group – 2)  NK  Digital Lab | | M App Practical (Group – 2)  BS  E\_Sim 1 Lab | |
| **Friday** | M App  BS  LT-21 | Electronic Circuits  SA  LT-21 | C Prog & DS  MT  E\_Sim 1 Lab | GE | Generic Practical | | | |

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| Subject | Electronic Circuits | | Dig E & Verilog – Digital Electronics and Verilog | | C Prog & DS – C Programming and Data Structures | | M App – Mobile Application | | GE – Generic |
| Teacher Code | SA – Ms Sonia Ahlawat | DJA – Mr. Deepak Jaiswal | NK – Dr. Neha Katyal | SUA – Ms. Surbhi Aggarwal | MT – Monika Tyagi | BS – Mr. Basant Saini | BS – Mr. Basant Saini | MT – Monika Tyagi |  |

Course: B.Sc. (Hons) Electronics V Semester wef 20.7.2019

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| **Time** | **9:00-10:00** | **10:00-11:00** | | **11:00-12:00** | **12:00-1:00** | **1:00-1:30** | **1:30-2:30** | **2:30-3:30** | **3:30-4:30** | **4:30-5:30** |
| **Day** | **1** | **2** | | **3** | **4** | **L**  **U**  **N**  **C**  **H**  **B**  **R**  **E**  **A**  **K** | **5** | **6** | **7** | **8** |
| **Monday** | DSP Practical  DJA  E\_Sim 2 Lab | | | NA  SUA  Digital Lab | DSP  DJA  Digital Lab | EMT  BS  Digital Lab | M&M  PRS  Digital Lab |  | |
|  | | Power Elect  BS  Digital Lab |  | EMT Practical (Group – E)  JY  E\_Sim 1 Lab | |
| **Tuesday** | EMT  NK  LT – 21 |  | | M&M Practical (Group-D)  PRS  E\_Sim 2 Lab | | NA  SUA  Digital Lab | M&M  PRS  Digital Lab |  |  |
| Power Electronics  MT  Digital Lab | | NA Practical (Group-E)  SUA  E\_Sim 1 Lab | |
| **Wednesday** | DSP  DJA  LT – 21 | DSP  DJA  LT – 21 | | EMT  NK  LT – 21 | NA  SUA  Digital Lab | NA Practical (Group-D)  SUA  E\_Sim 2 Lab | |  |  |
| M&M Practical(Group – E)  PRS  E\_Sim1 Lab | | | EMT Practical (Group – E)  JY  E\_Sim1 Lab | |
| **Thursday** | EMT Practical (Group – D)  NK  E\_Sim 2 Lab | | | NA  SUA  E\_Sim 1 Lab | DSP  DJA  Analog Lab | M&M  PRS  LT – 21 | M&M Practical (Group-D)  PRS  E\_Sim 2 Lab | |  |
| NA Practical (Group-E)  SUA  E\_Sim 1 Lab | | | Power Elect  MT  E\_Sim 1 Lab | Power Elect  BS  LT – 21 |  |
| **Friday** | EMT Practical (Group – D)  NK  E\_Sim 2 Lab | | | EMT  BS  Digital Lab | M&M  PRS  Digital Lab | NA Practical (Group – D)  SUA  E\_Sim 2 Lab | | DSP Practical  DJA  E\_Sim 2 Lab | |
| M&M Practical(Group – E)  PRS  E\_Sim 1 Lab | | | Power Electronics Practical  MT  Circuits Network Lab | | | |

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| Subject | M&M – Microprocessors and Microcontrollers | EMT – Electromagnetics | | Power Elect – Power Electronics | | NA – Numerical Analysis | DSP – Digital Signal Processing |
| Teachers Code | PS – Ms. Preeti Singhal | NK – Dr. Neha Katyal | JY – Ms. Jyoti | MT – Ms. Monika Tyagi | BS – Mr. Basant Saini | SUA – Ms. Surbhi Aggarwal | DJA – Mr. Deepak Jaiswal |